

FIG. 1

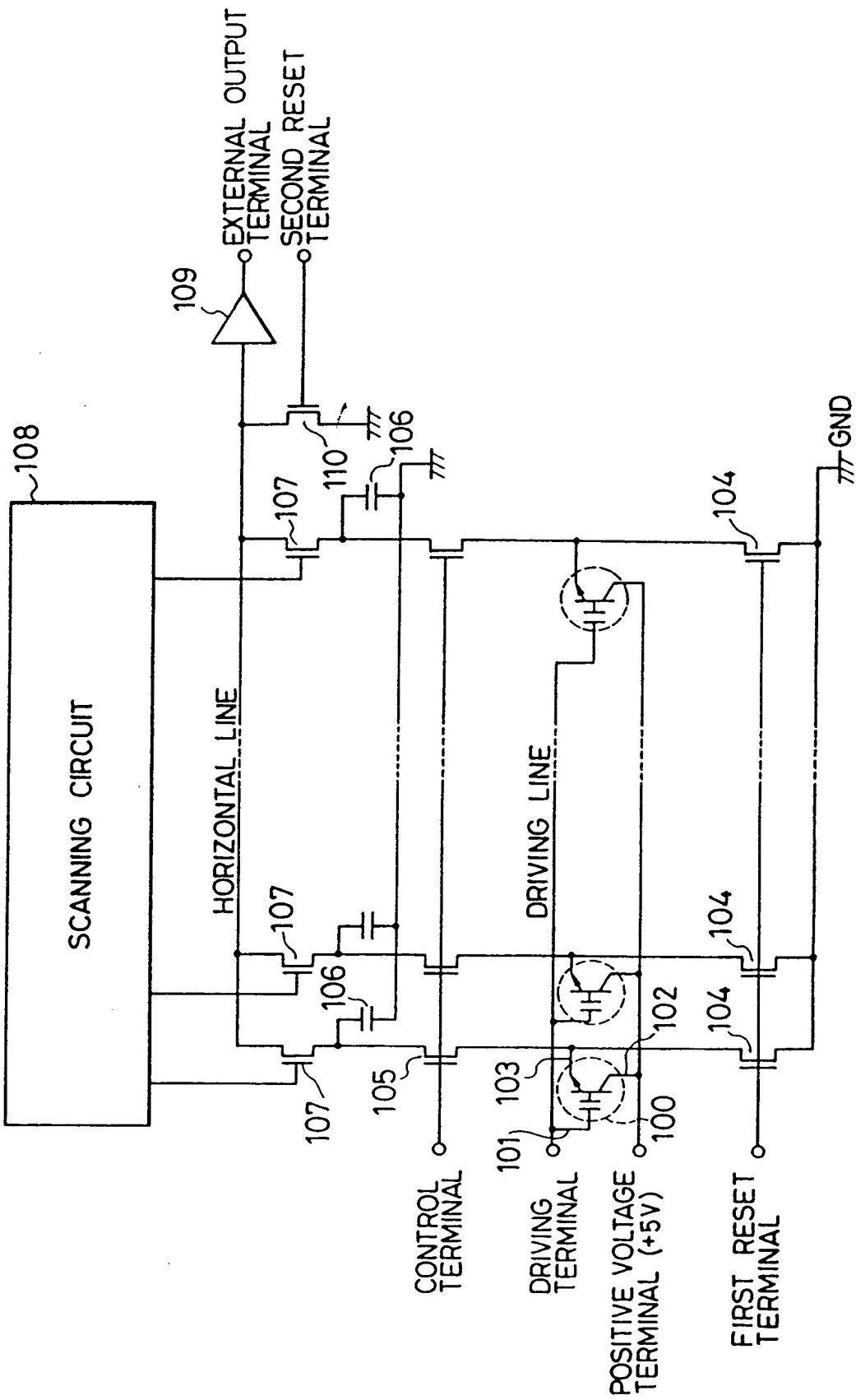


FIG. 2

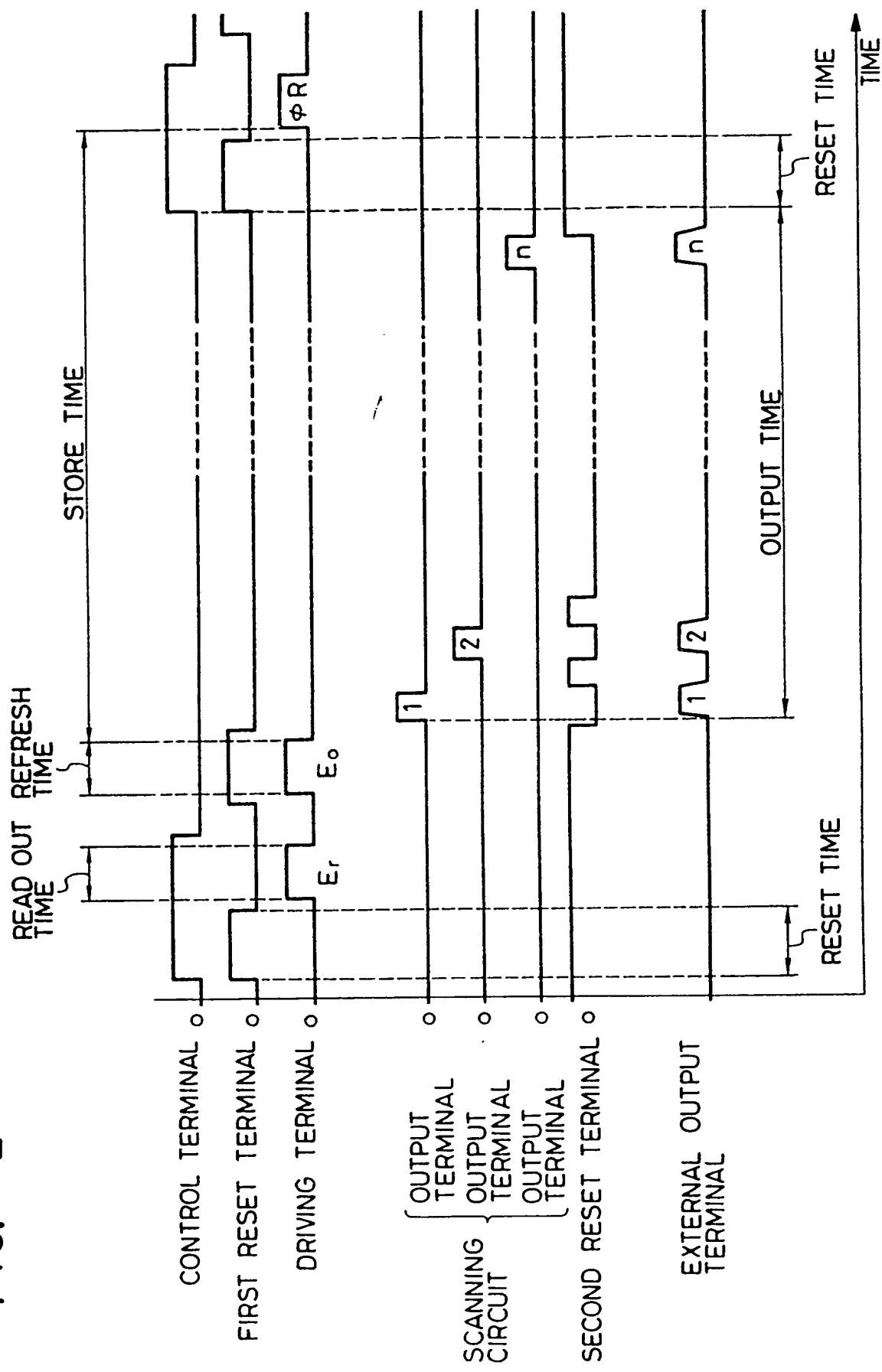


FIG. 3A

CONTROL CIRCUIT

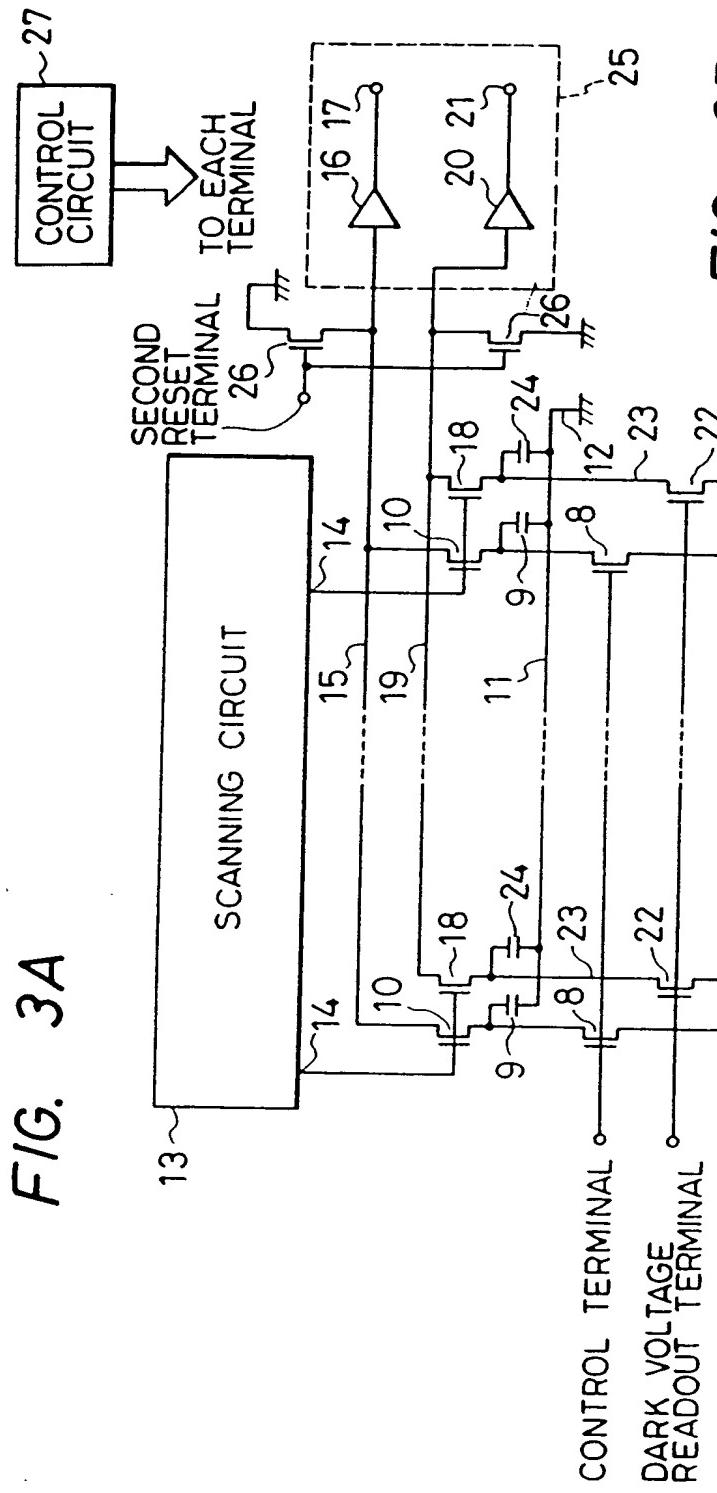


FIG. 3B

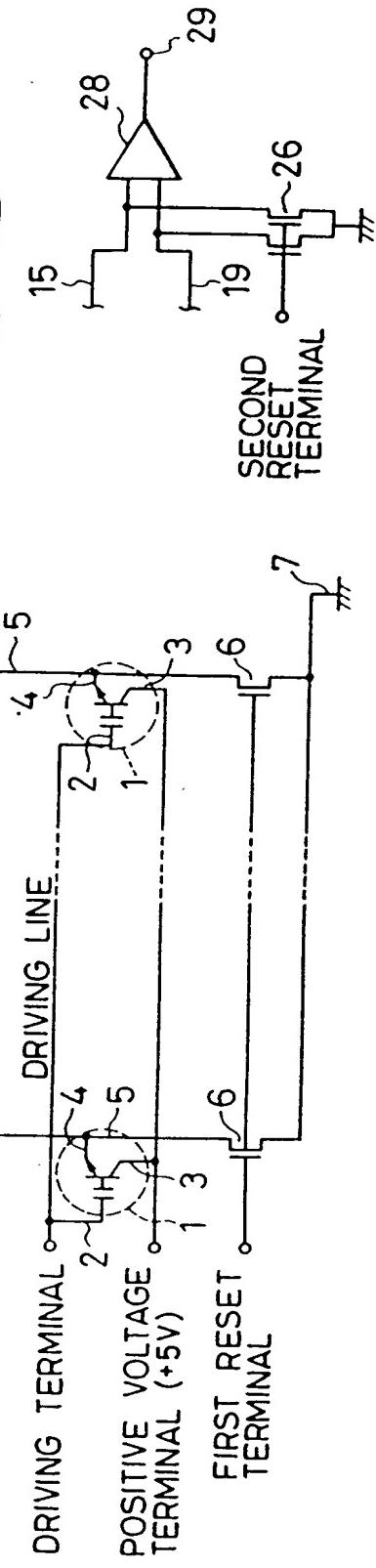


FIG. 4

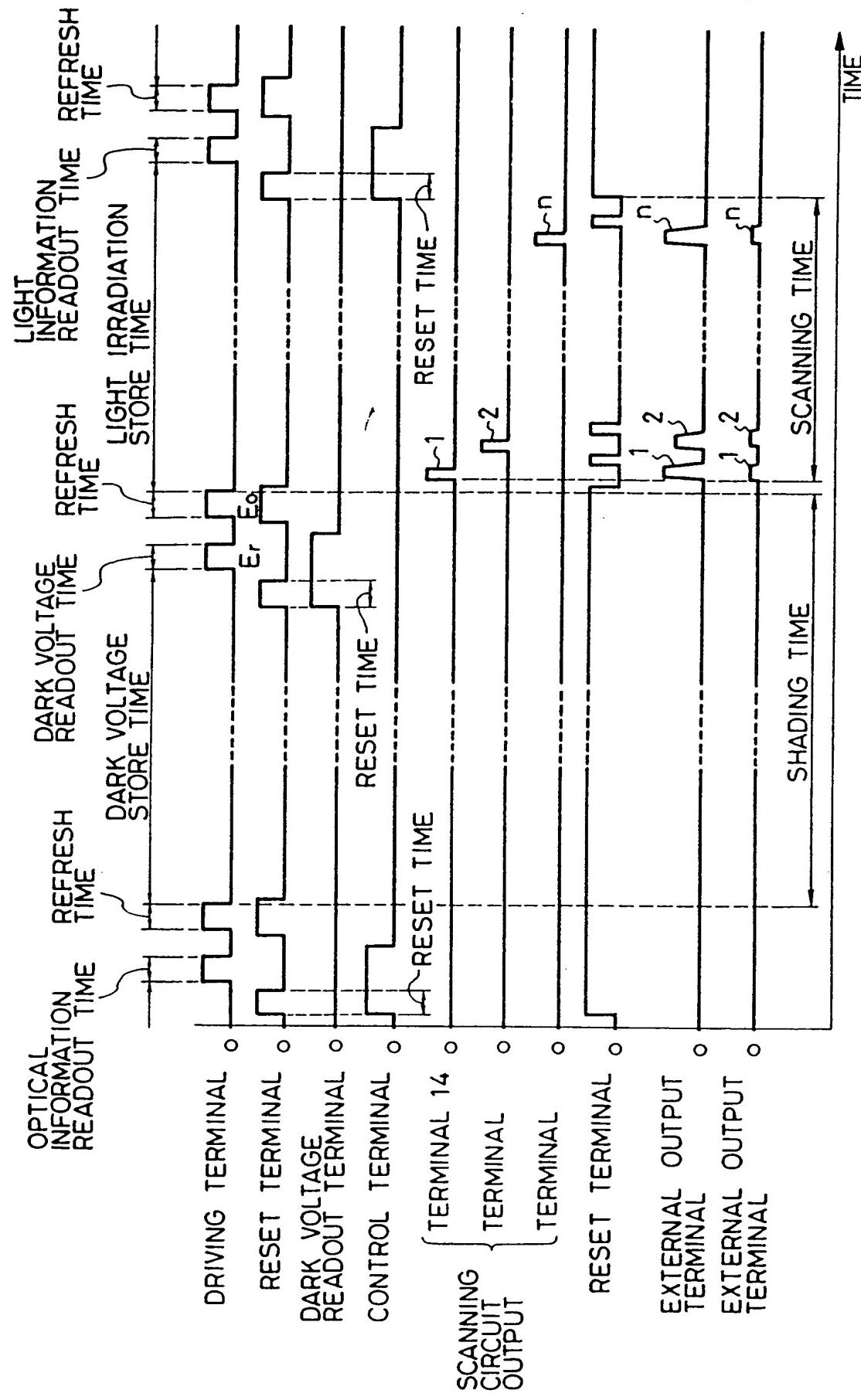
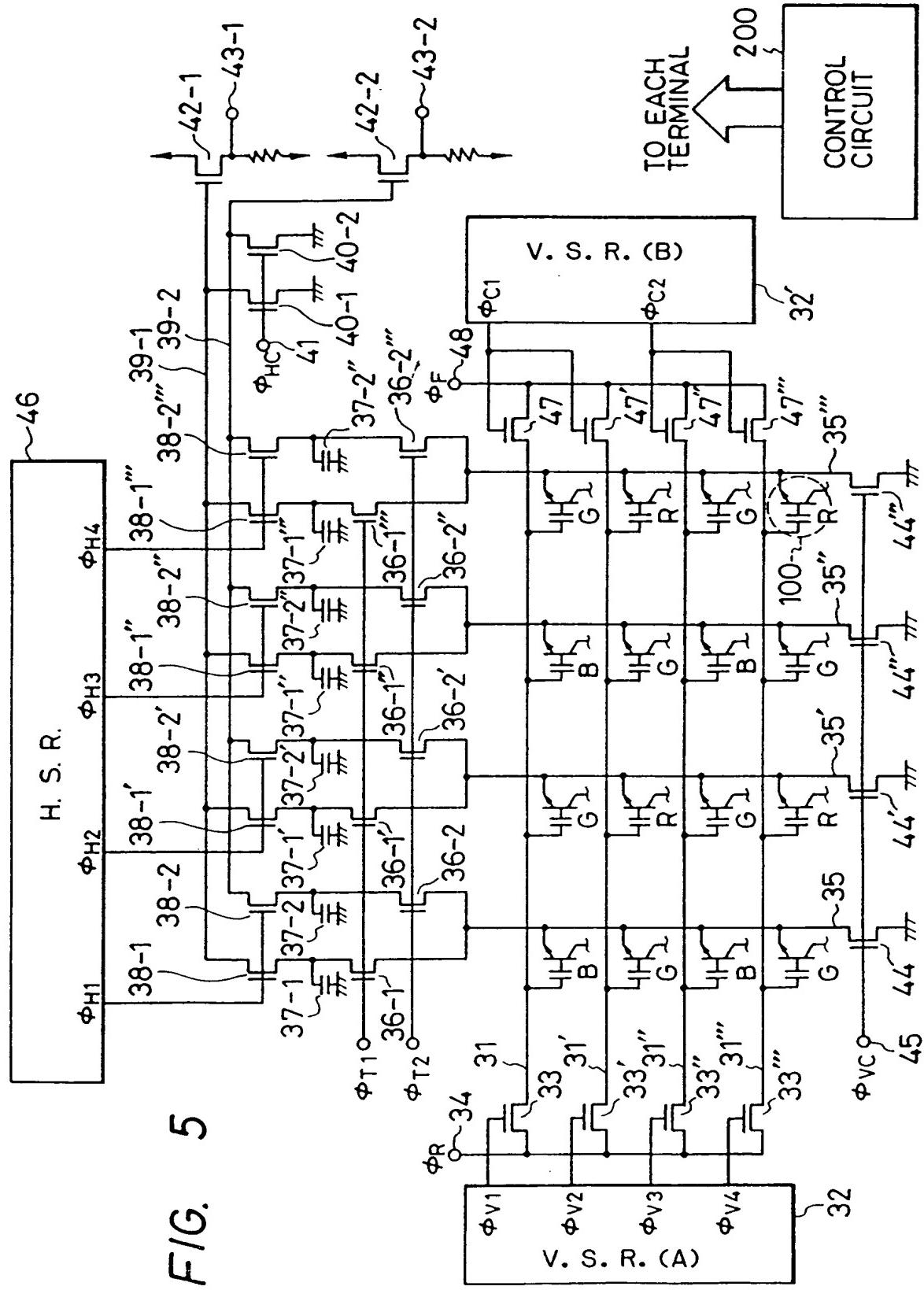


FIG.

5



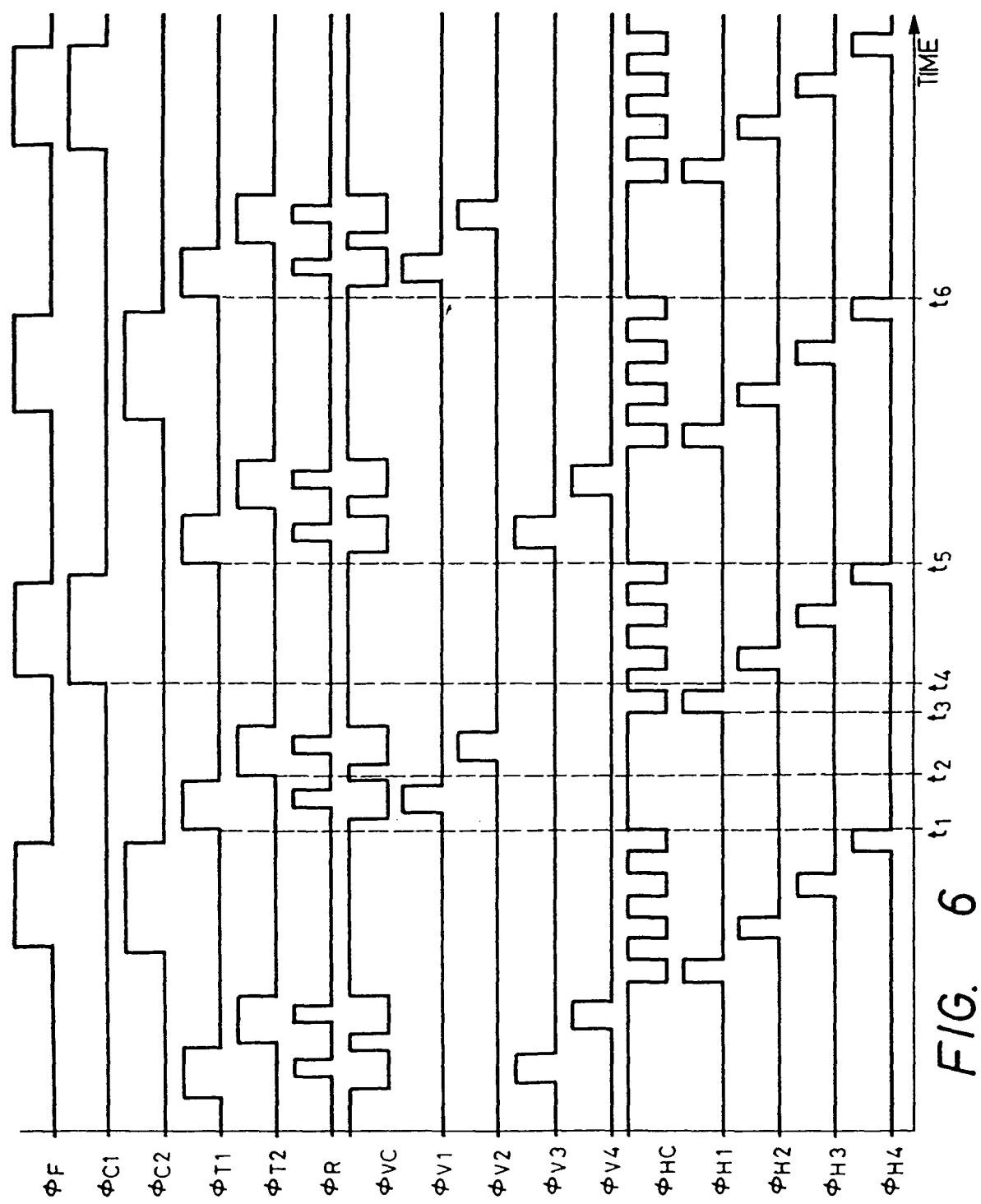


FIG. 6

~46

FIG. 7

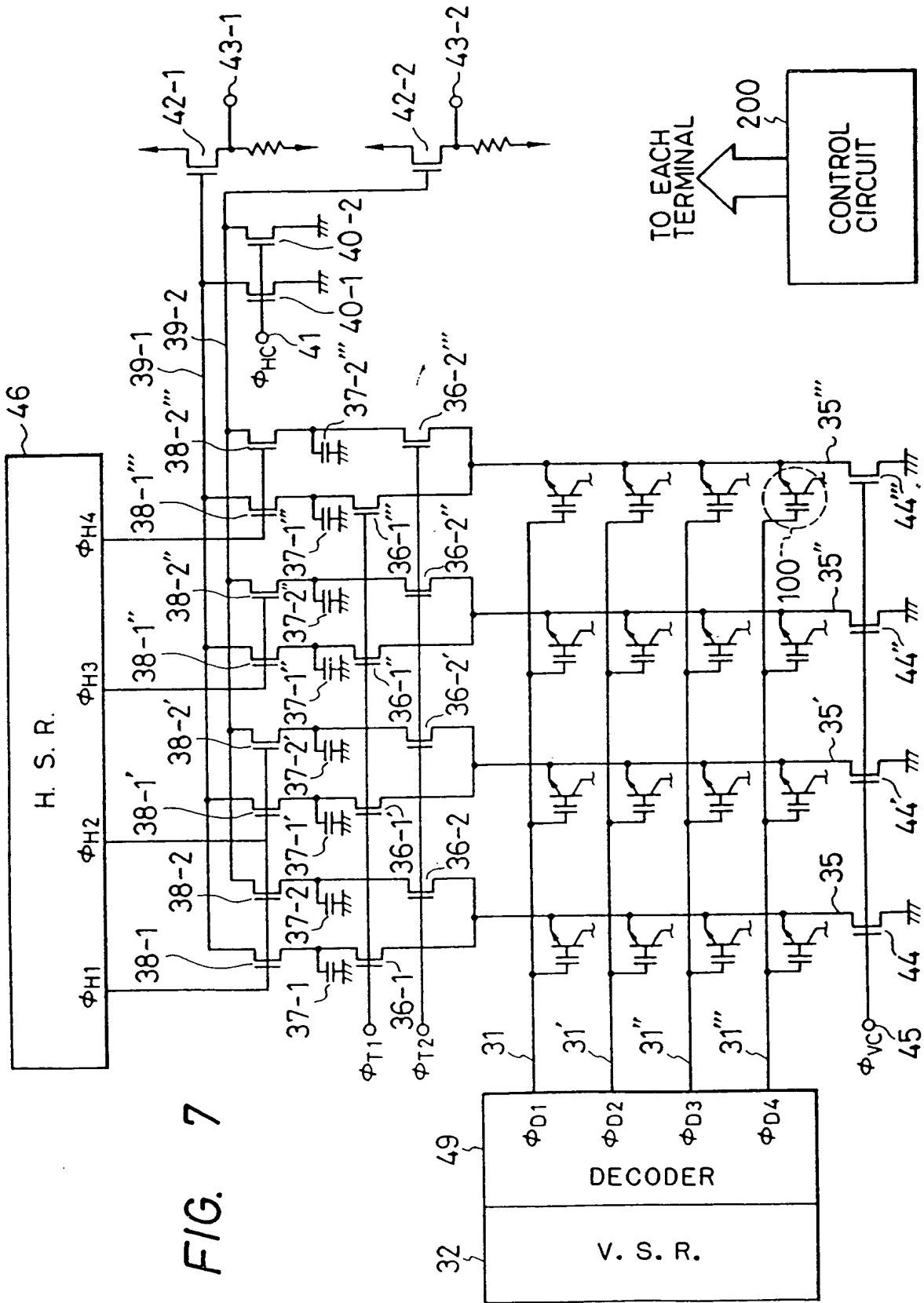


FIG. 8

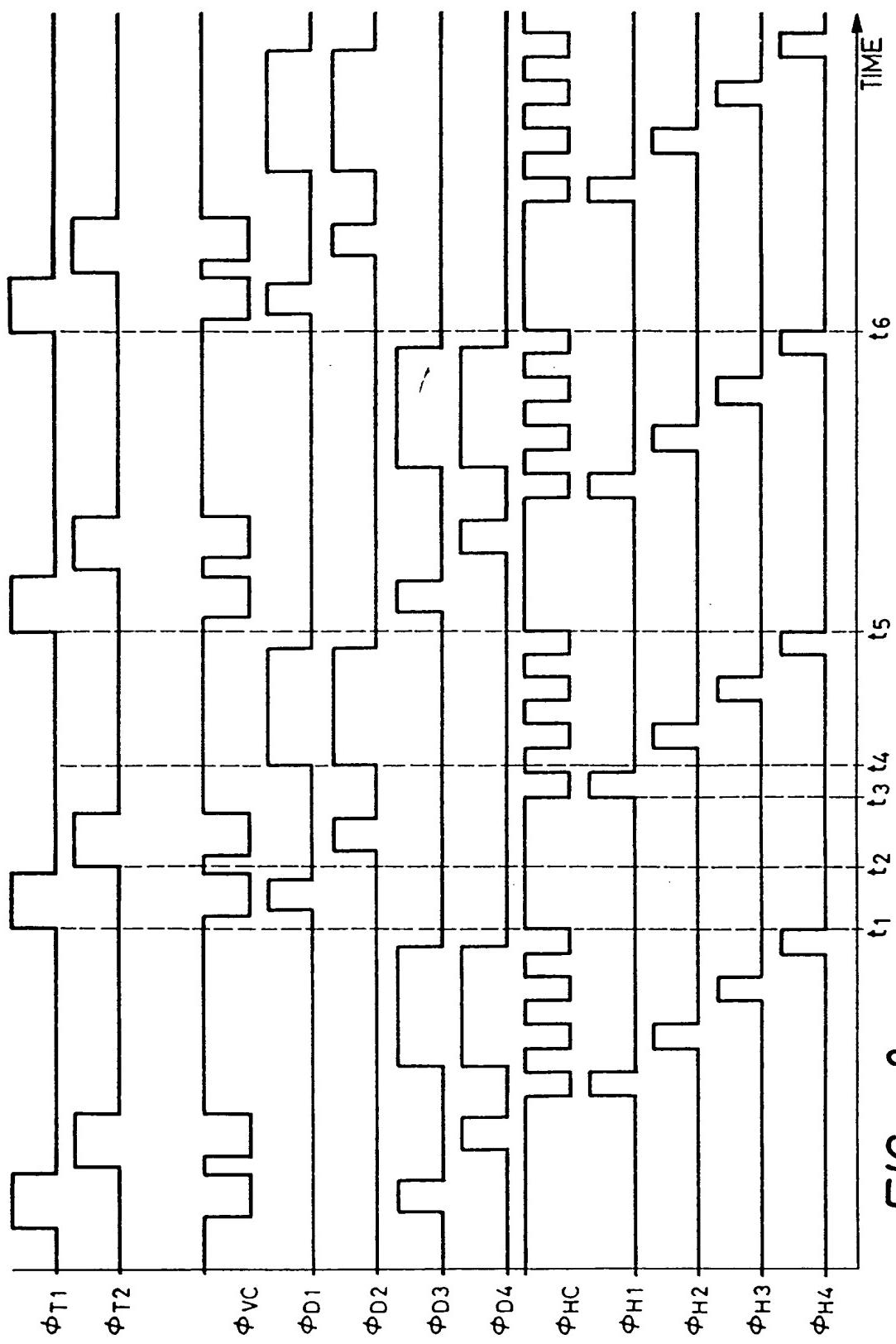
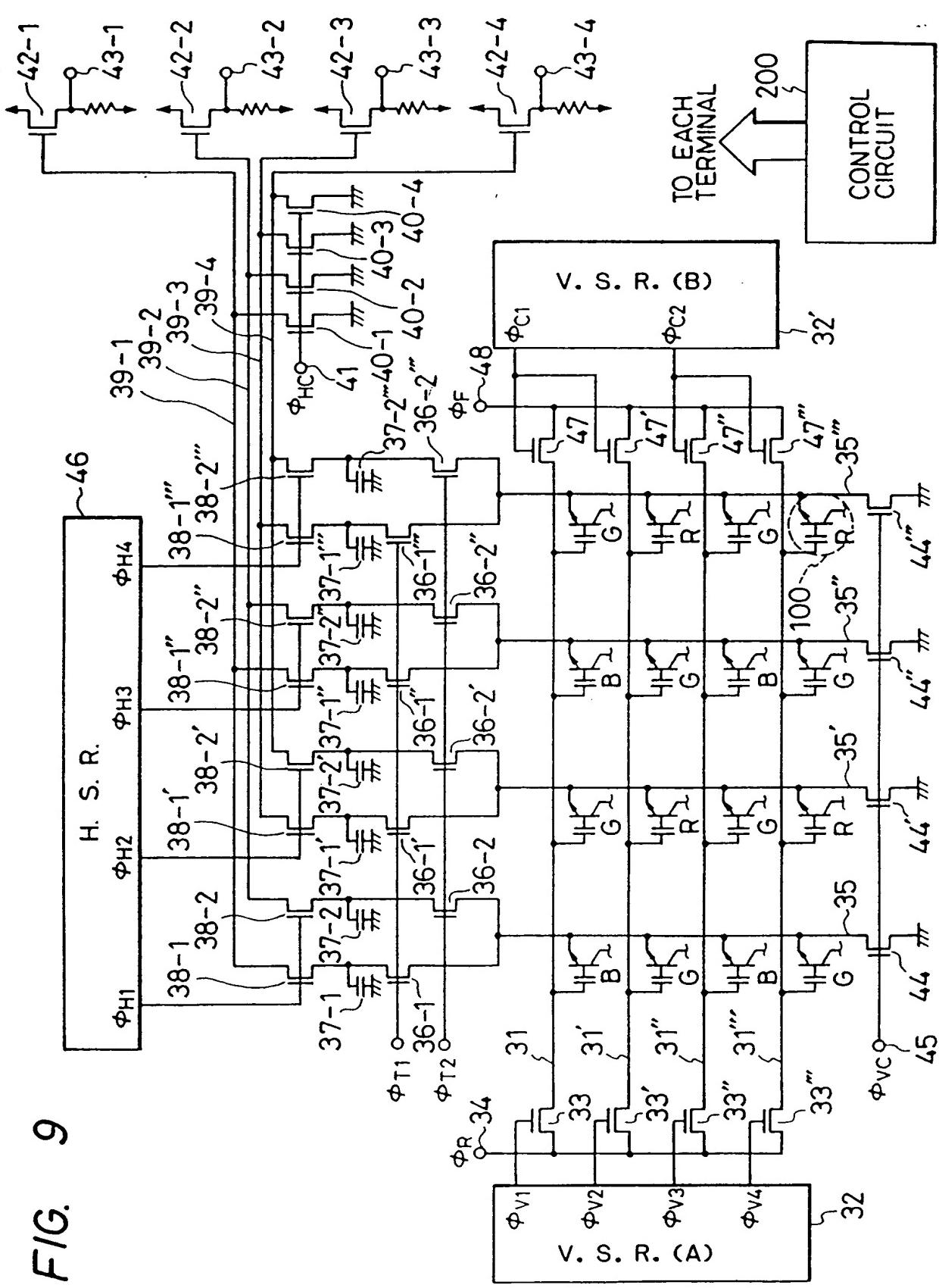


FIG. 9



*FIG. 10*

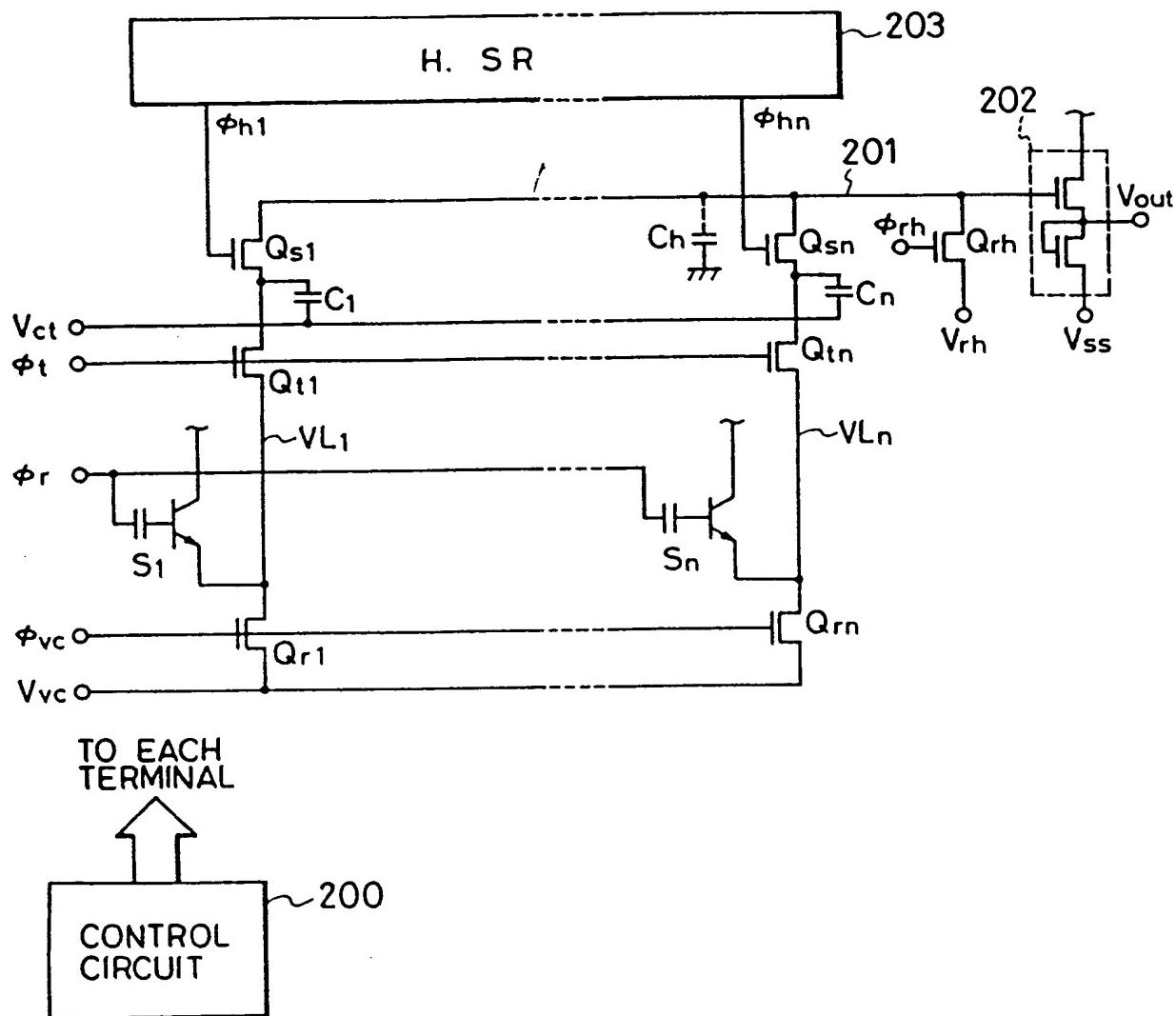
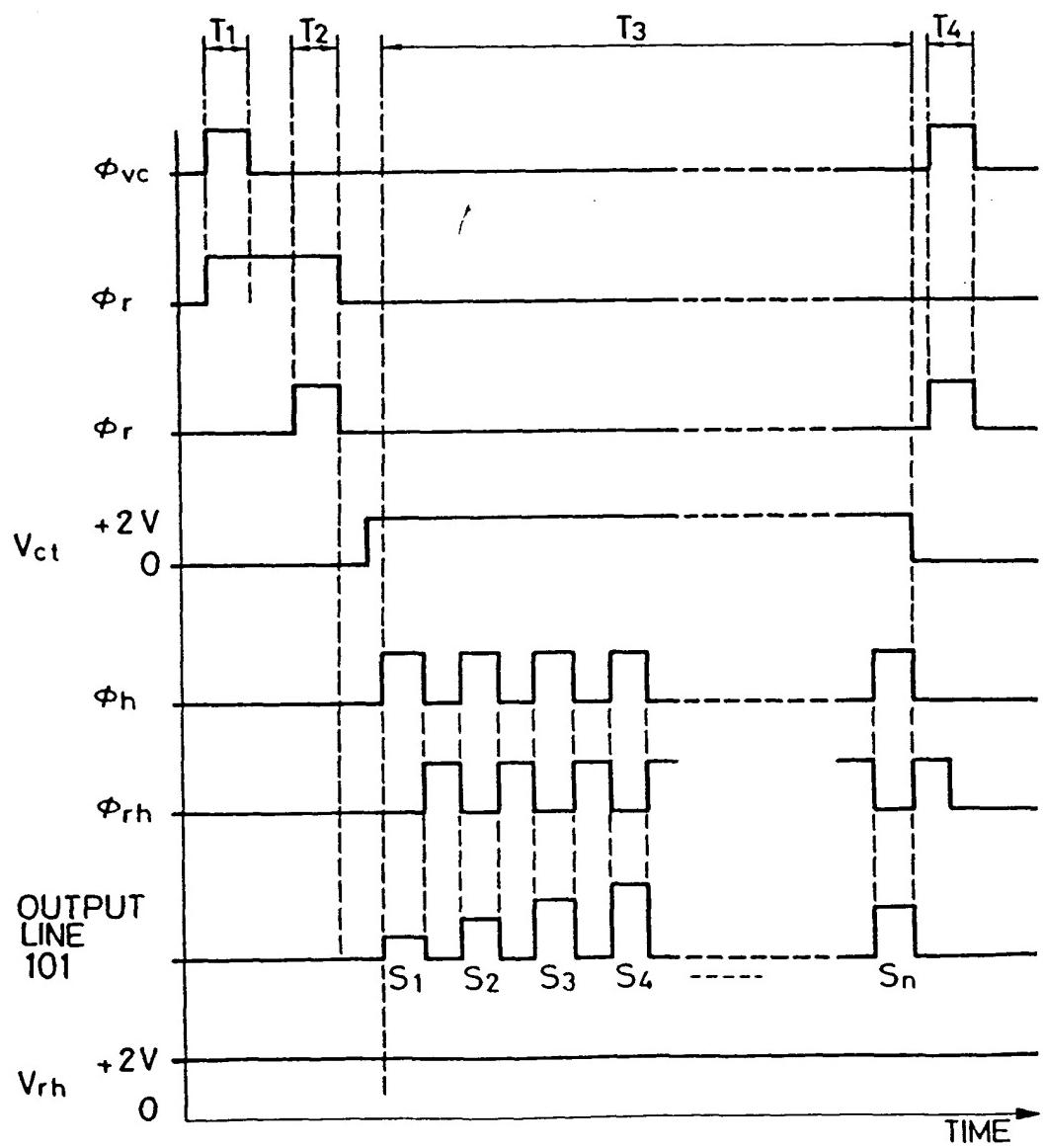
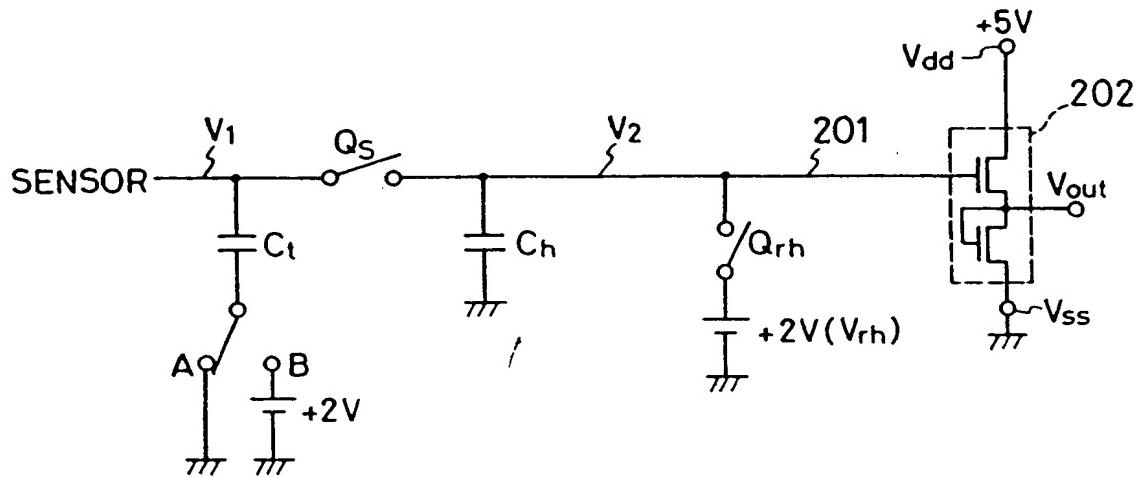


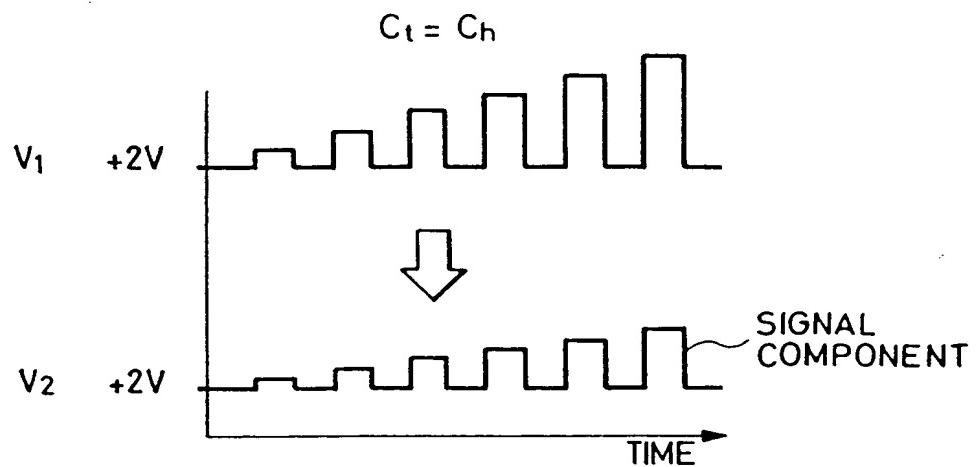
FIG. 11



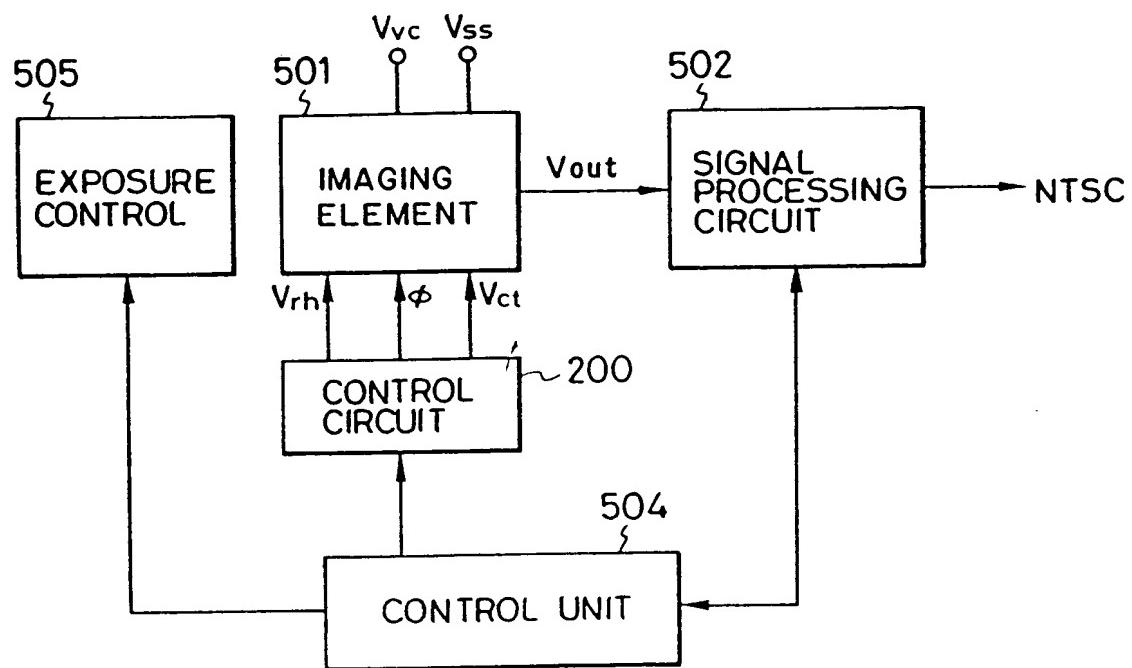
*FIG. 12A*



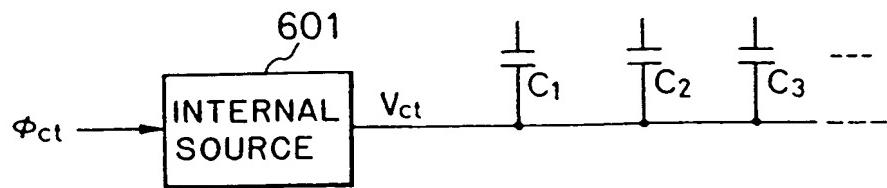
*FIG. 12B*



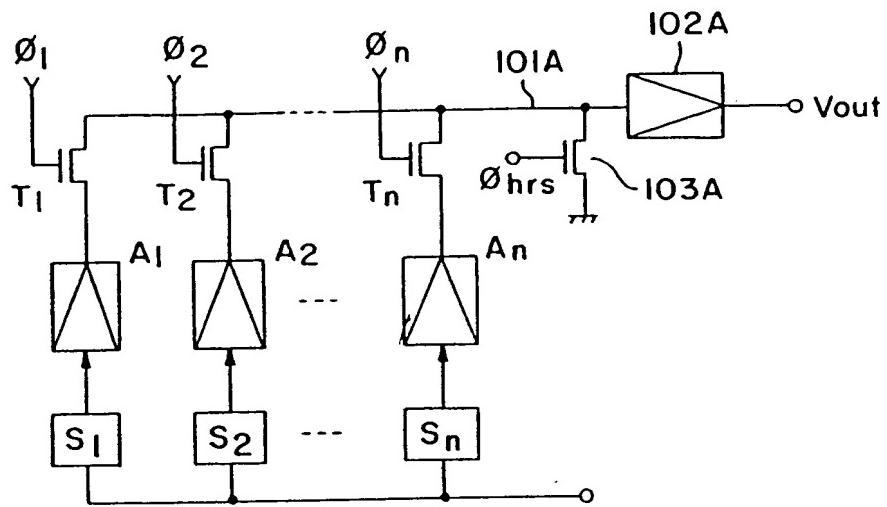
*FIG. 13*



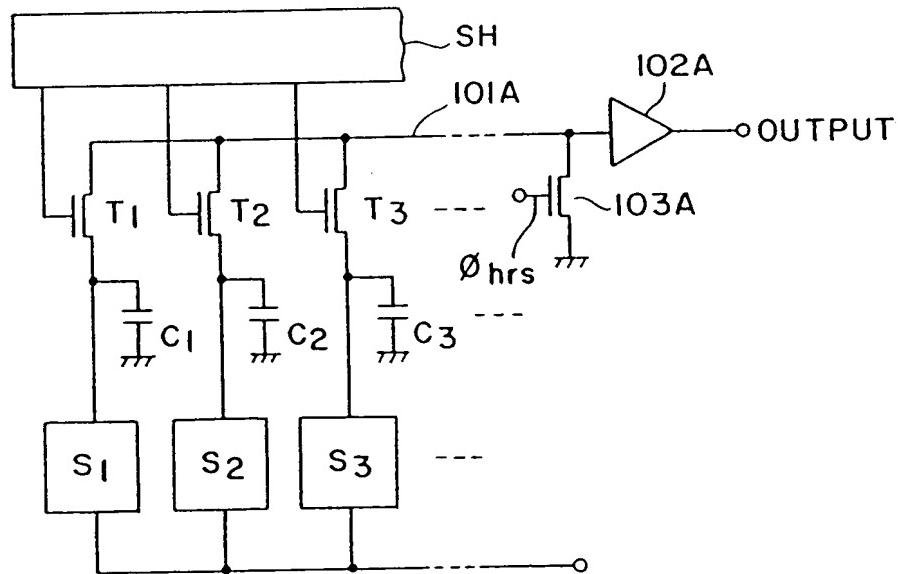
*FIG. 14*



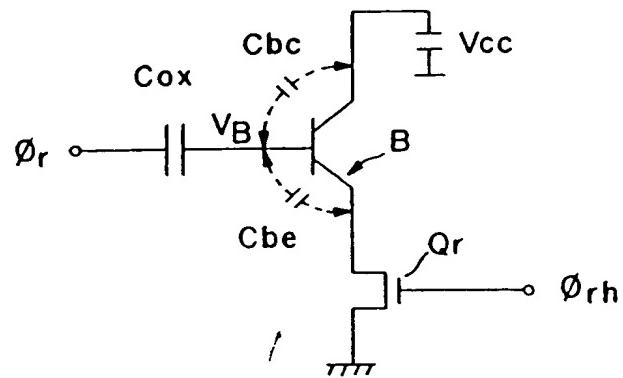
*FIG. 15A* PRIOR ART



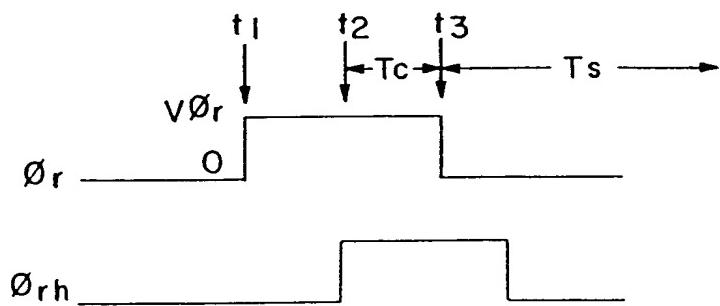
*FIG. 15B* PRIOR ART



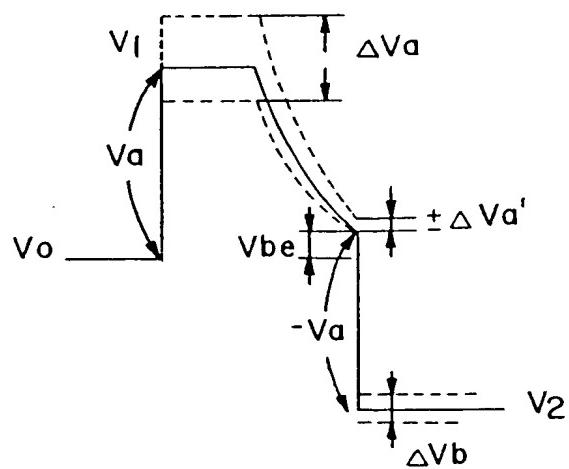
*FIG. 15C* PRIOR ART



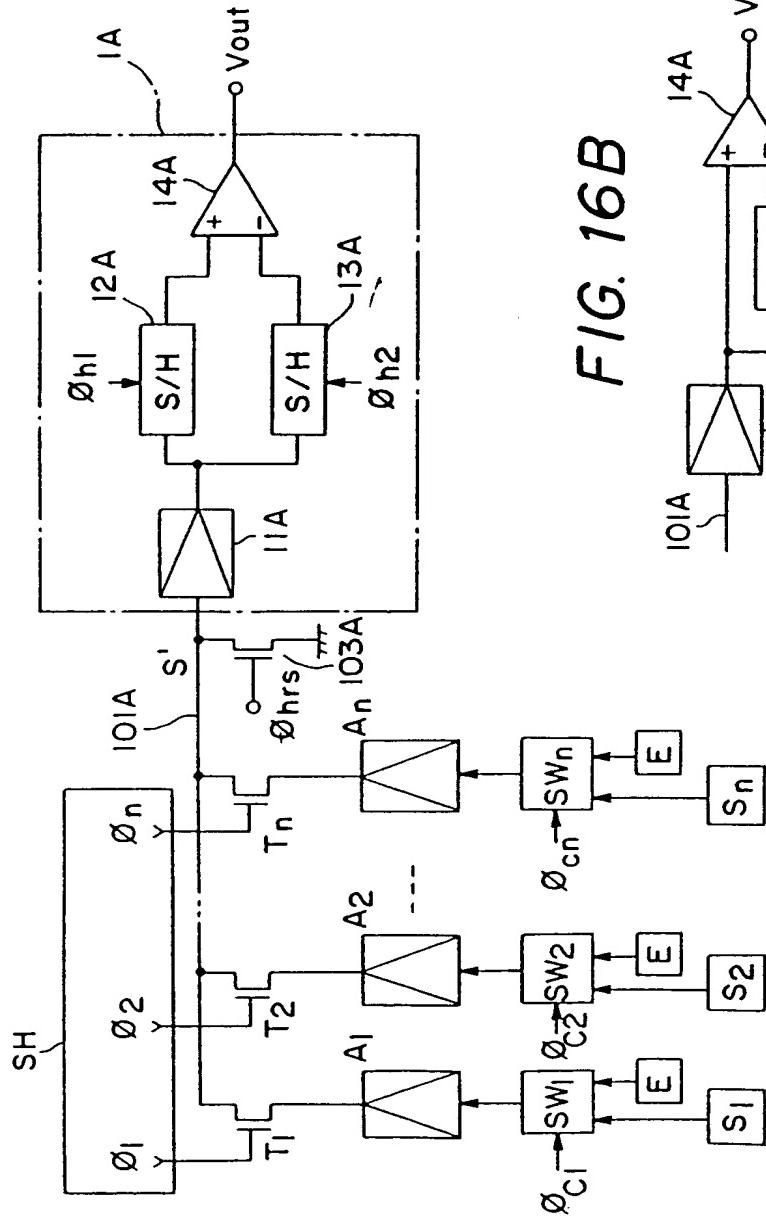
*FIG. 15D* PRIOR ART



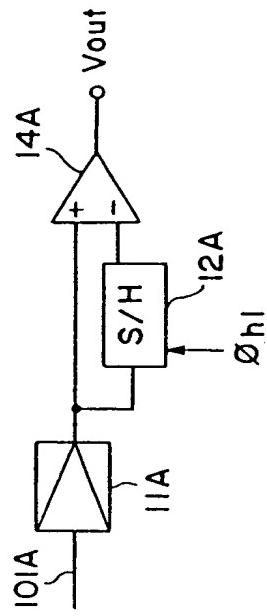
*FIG. 15E* PRIOR ART



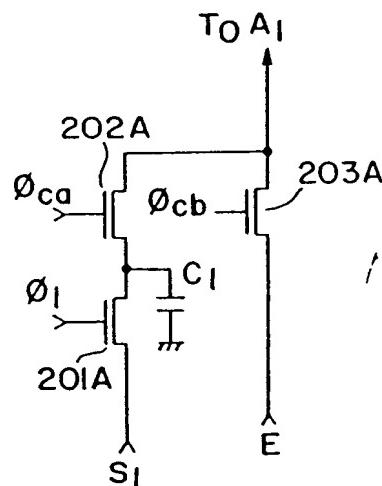
*FIG. 16A*



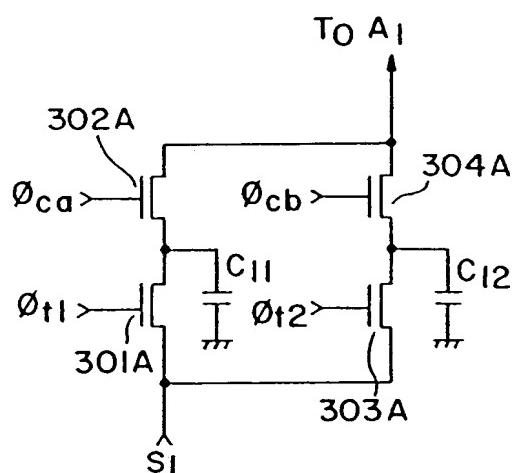
*FIG. 16B*



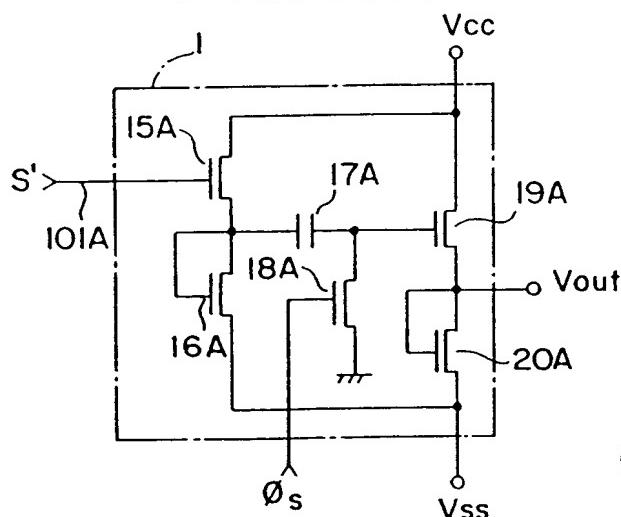
*FIG. 17*



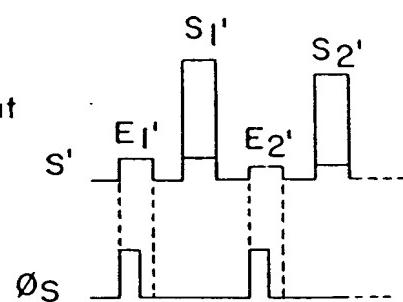
*FIG. 18*



*FIG. 19A*



*FIG. 19B*



*FIG. 20A*

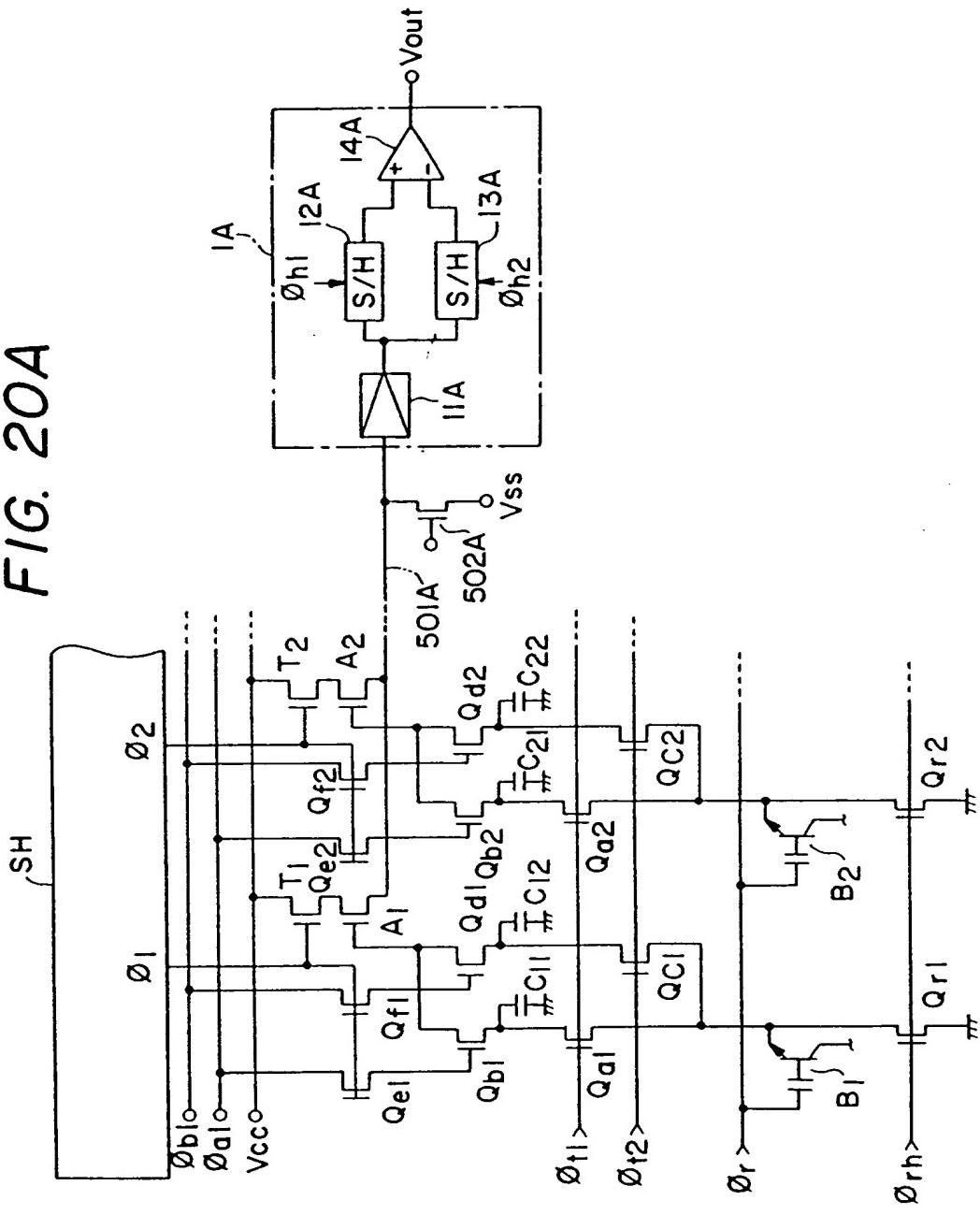
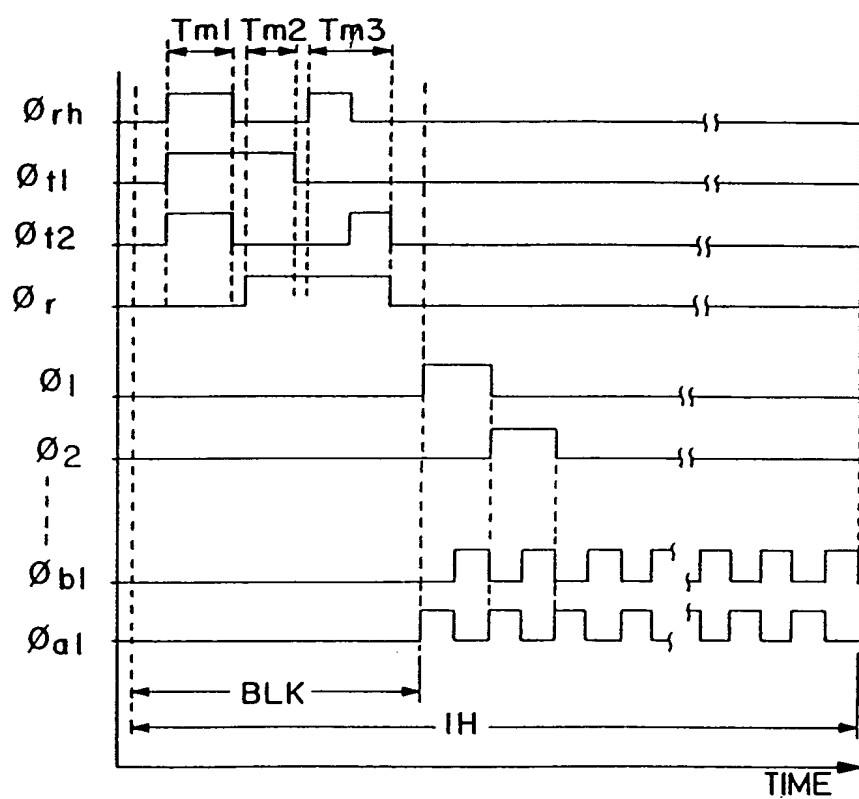
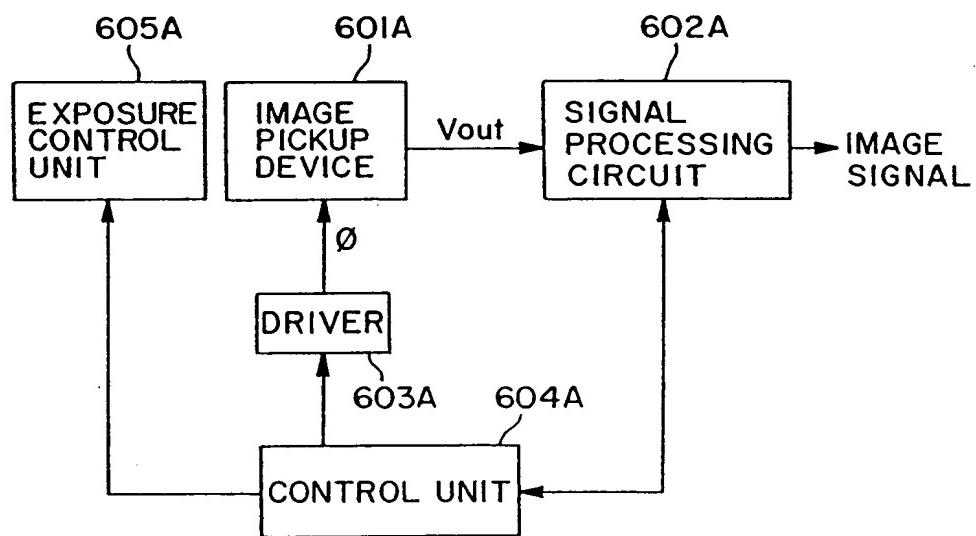


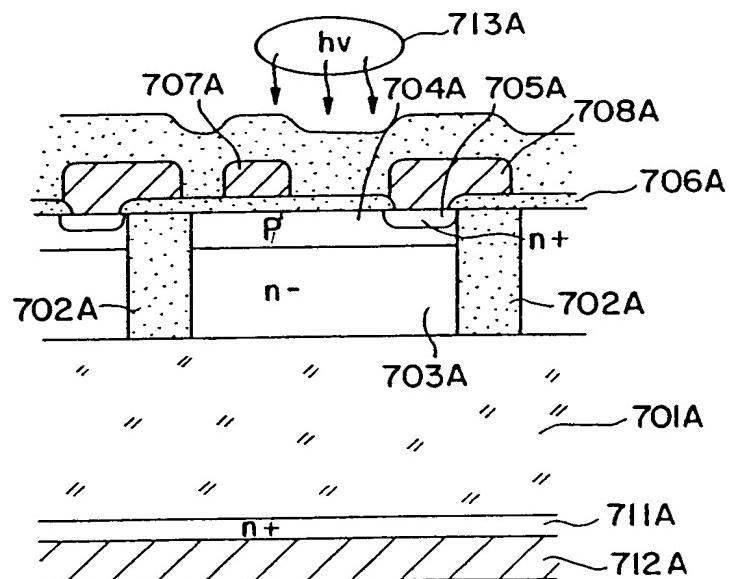
FIG. 20B



*FIG. 21*



*FIG. 22A* PRIOR ART



*FIG. 22B* PRIOR ART

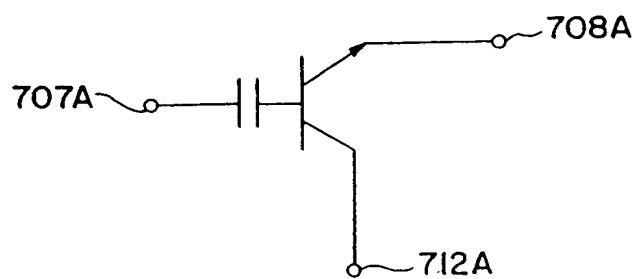


FIG. 23

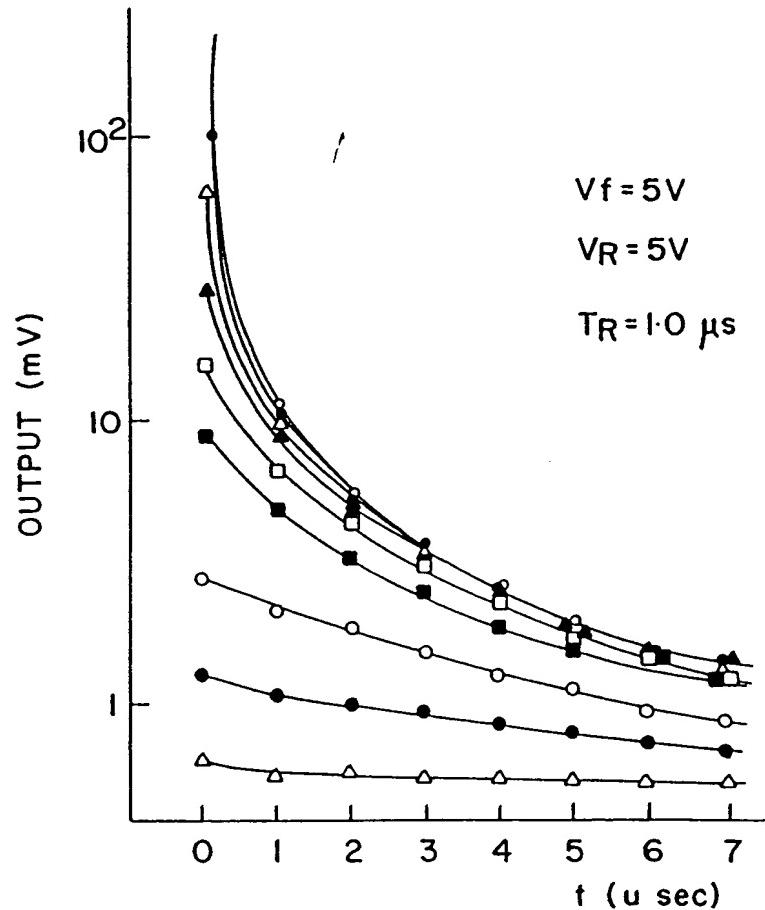


FIG. 24

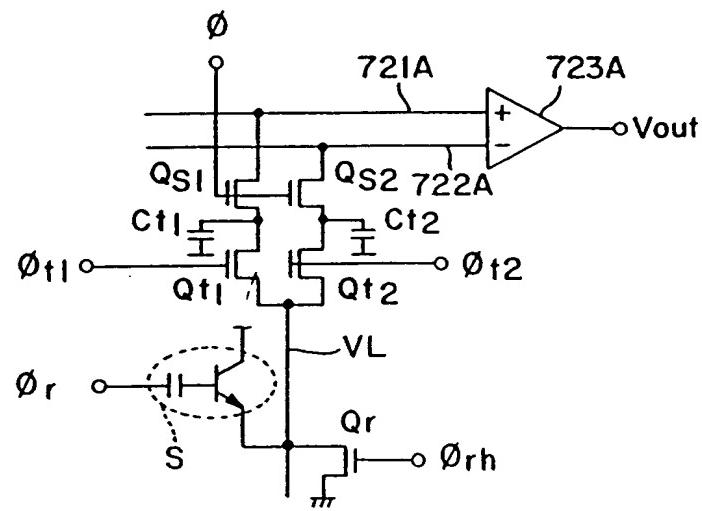
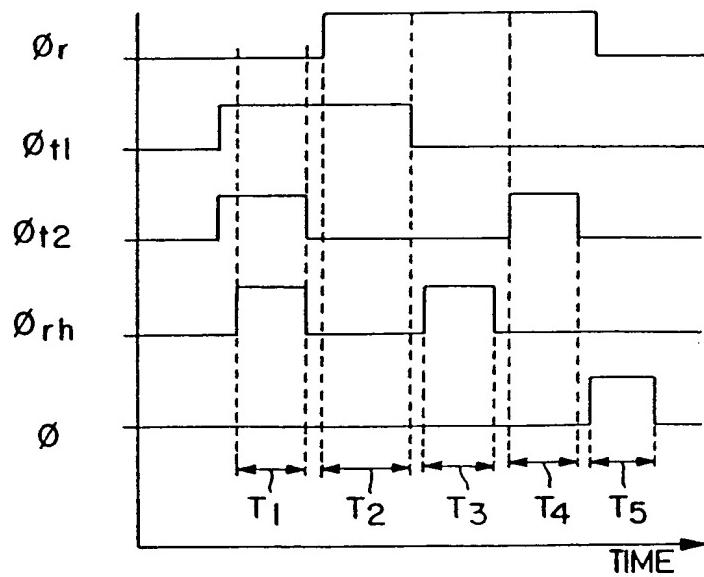
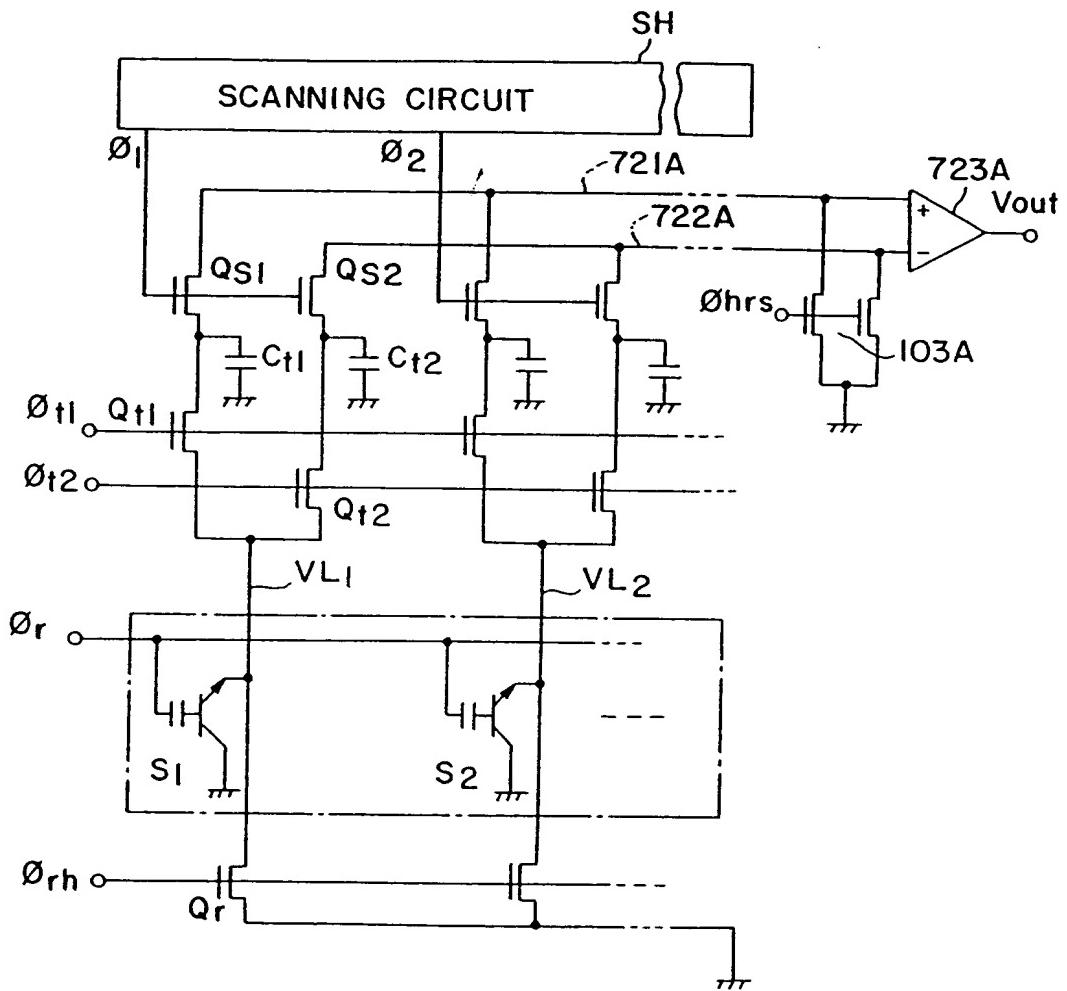


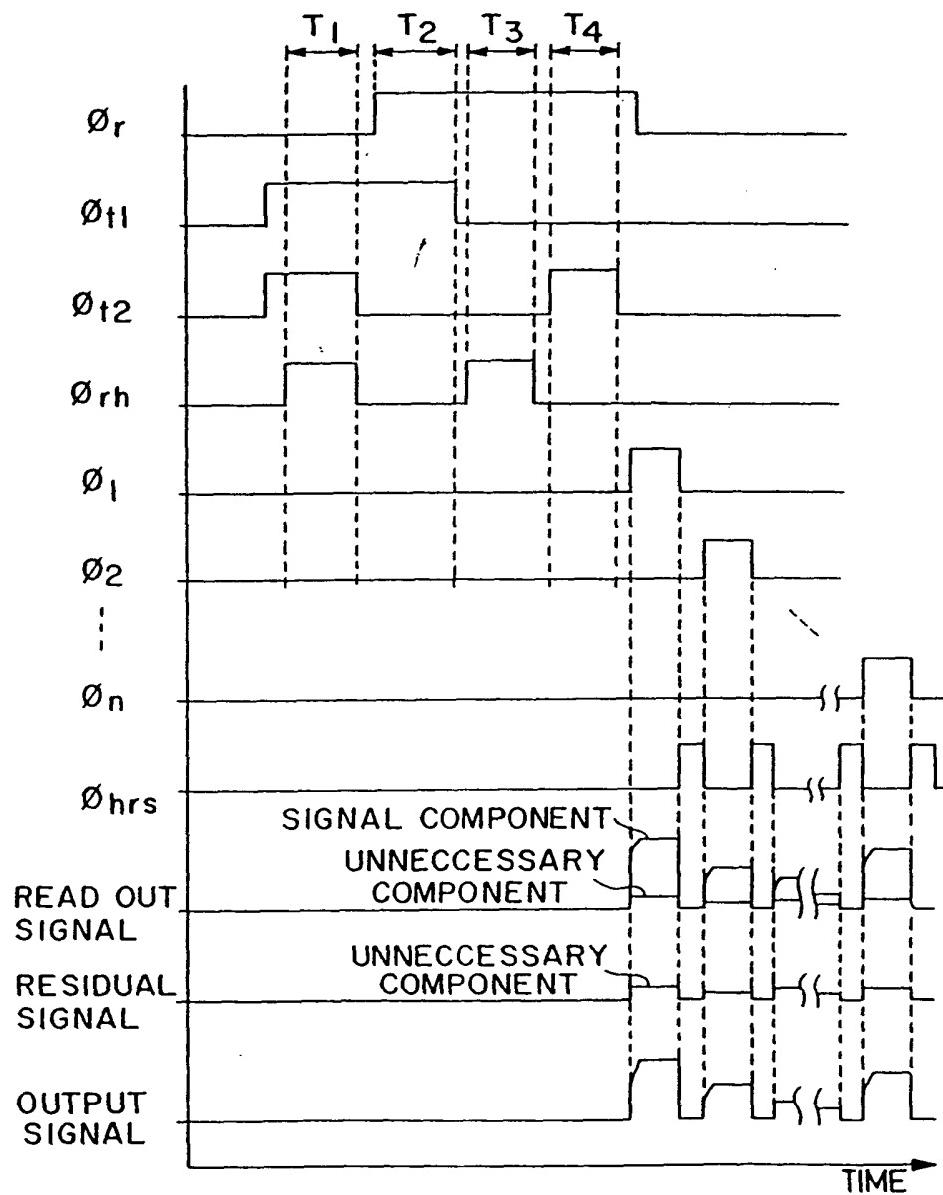
FIG. 25



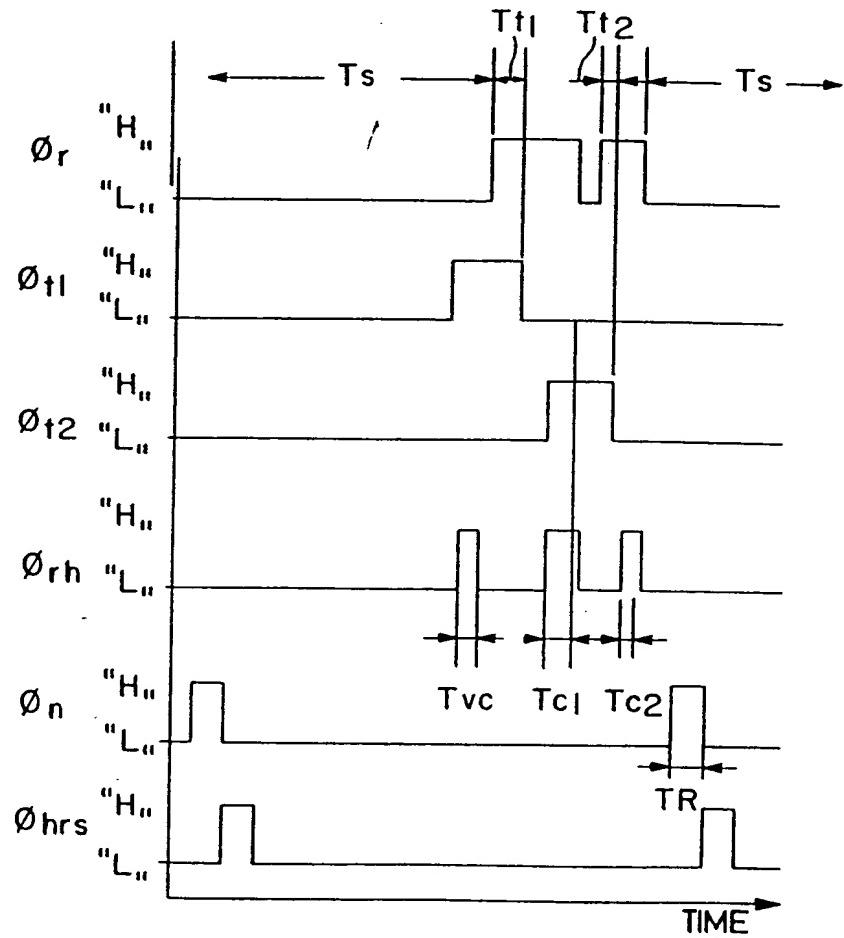
*FIG. 26*



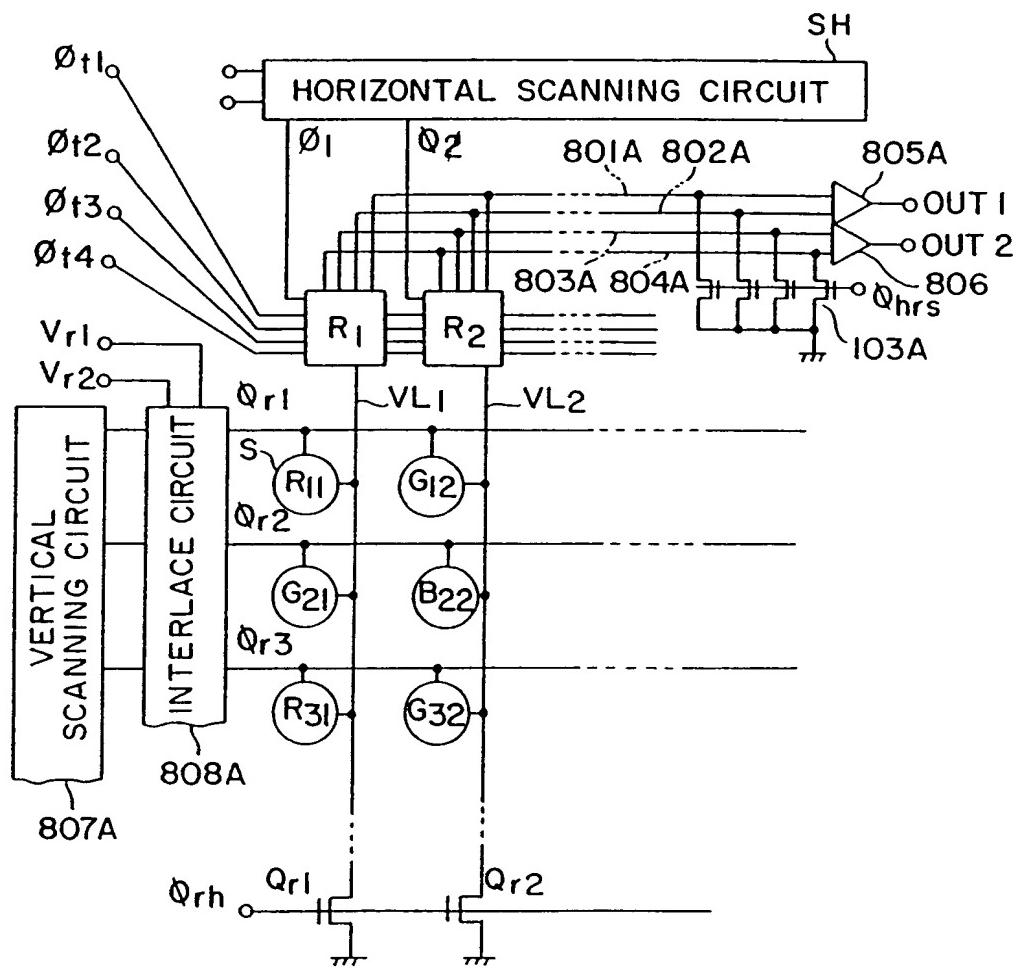
*FIG. 27A*



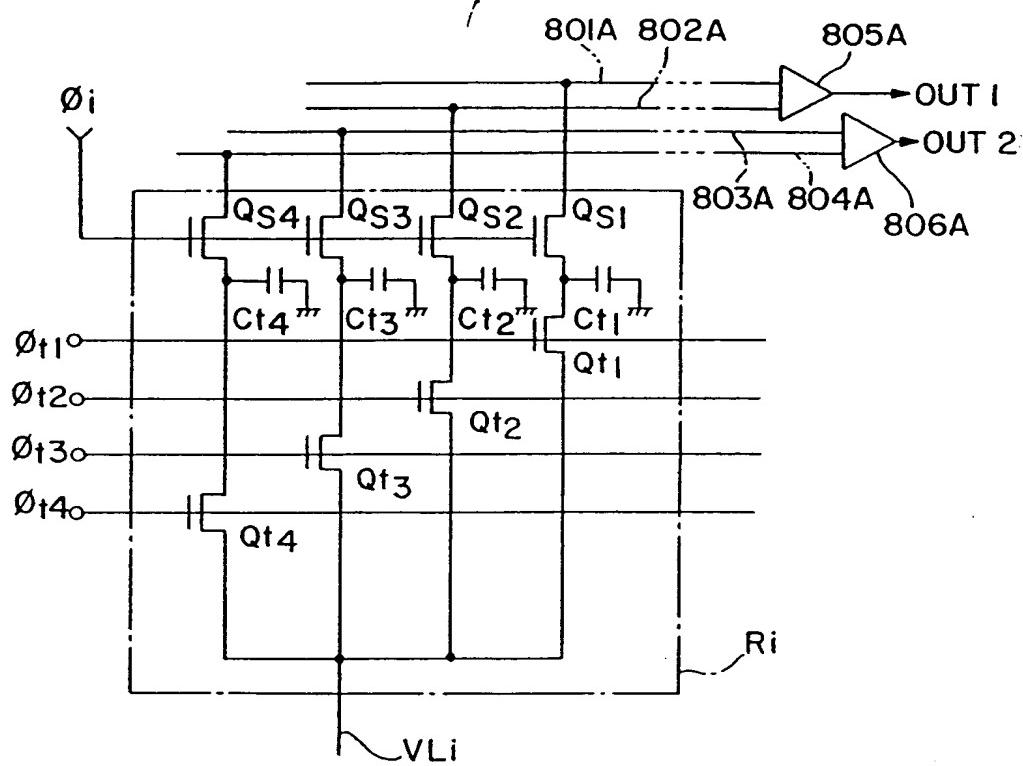
*FIG. 27B*



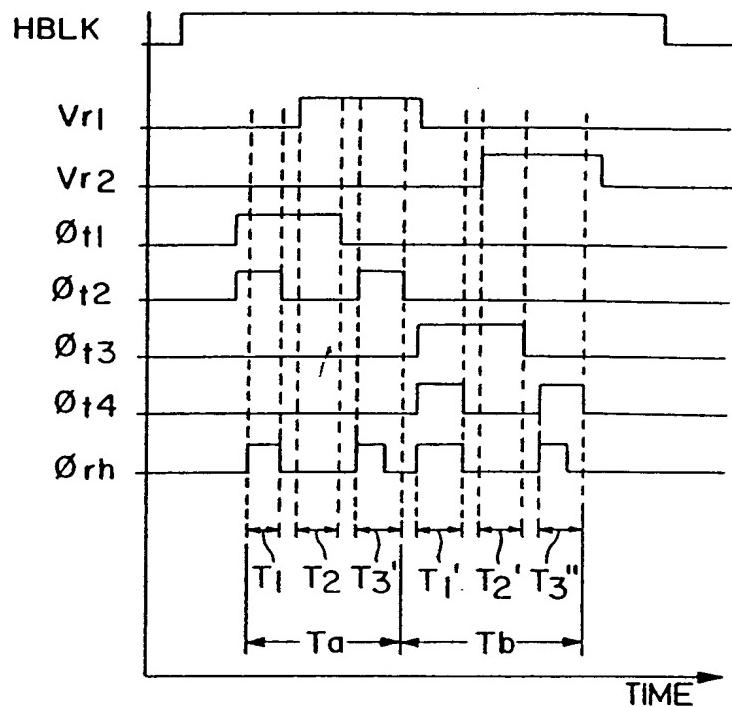
*FIG. 28*



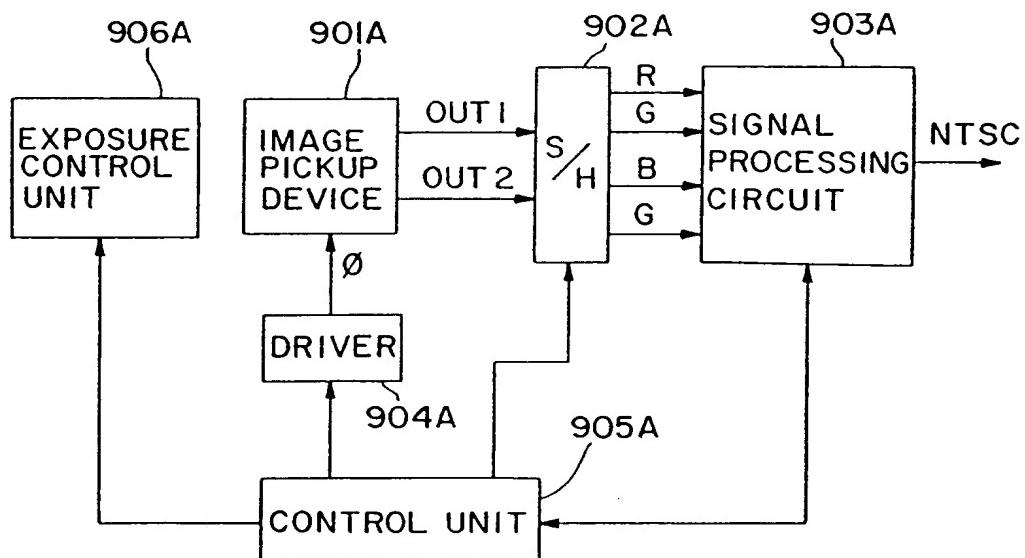
*FIG. 29*



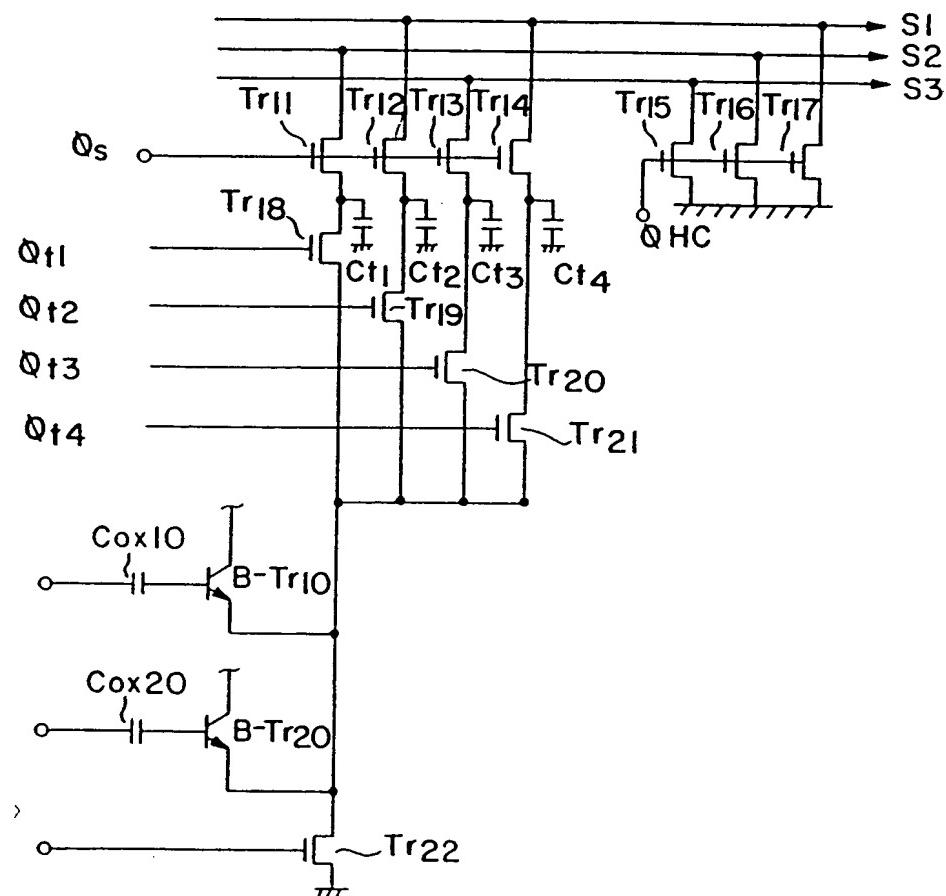
*FIG. 30*



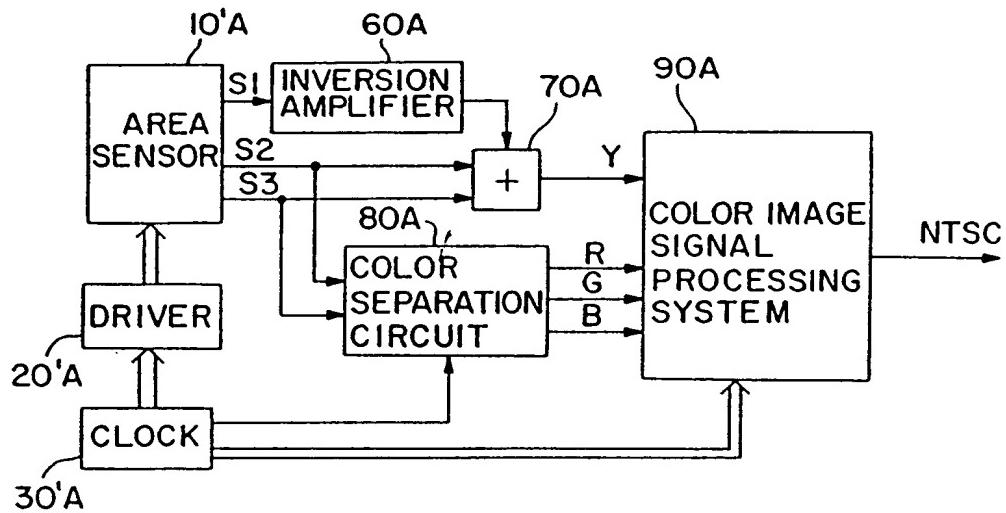
*FIG. 31*



*FIG. 32*



*FIG. 33*



*FIG. 34*

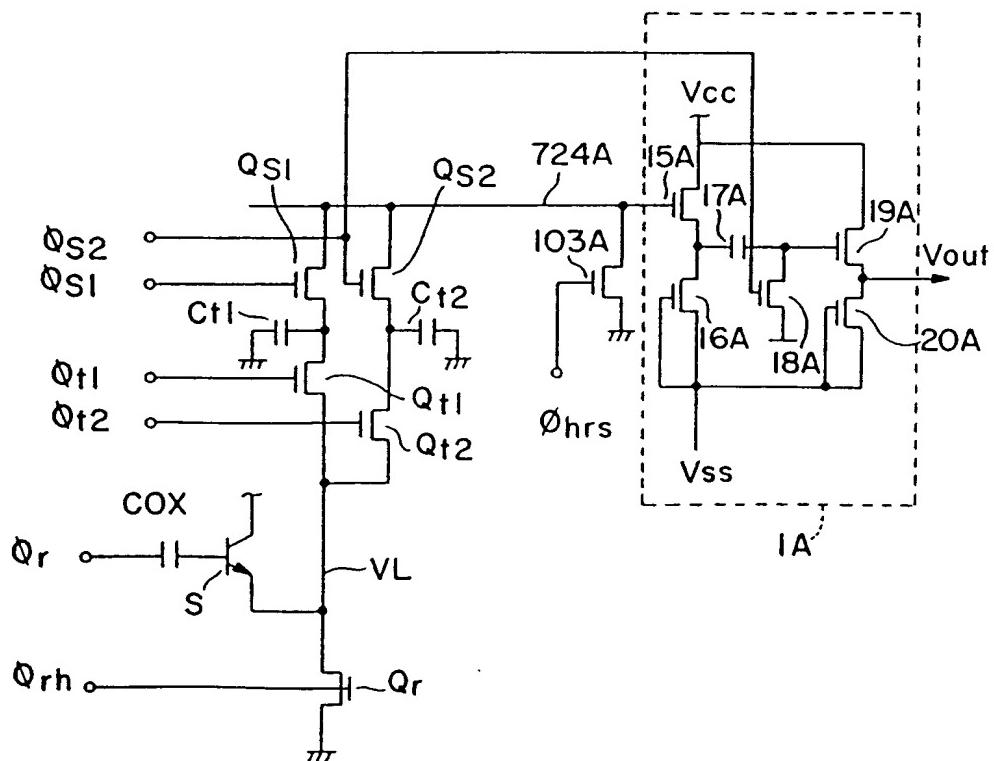
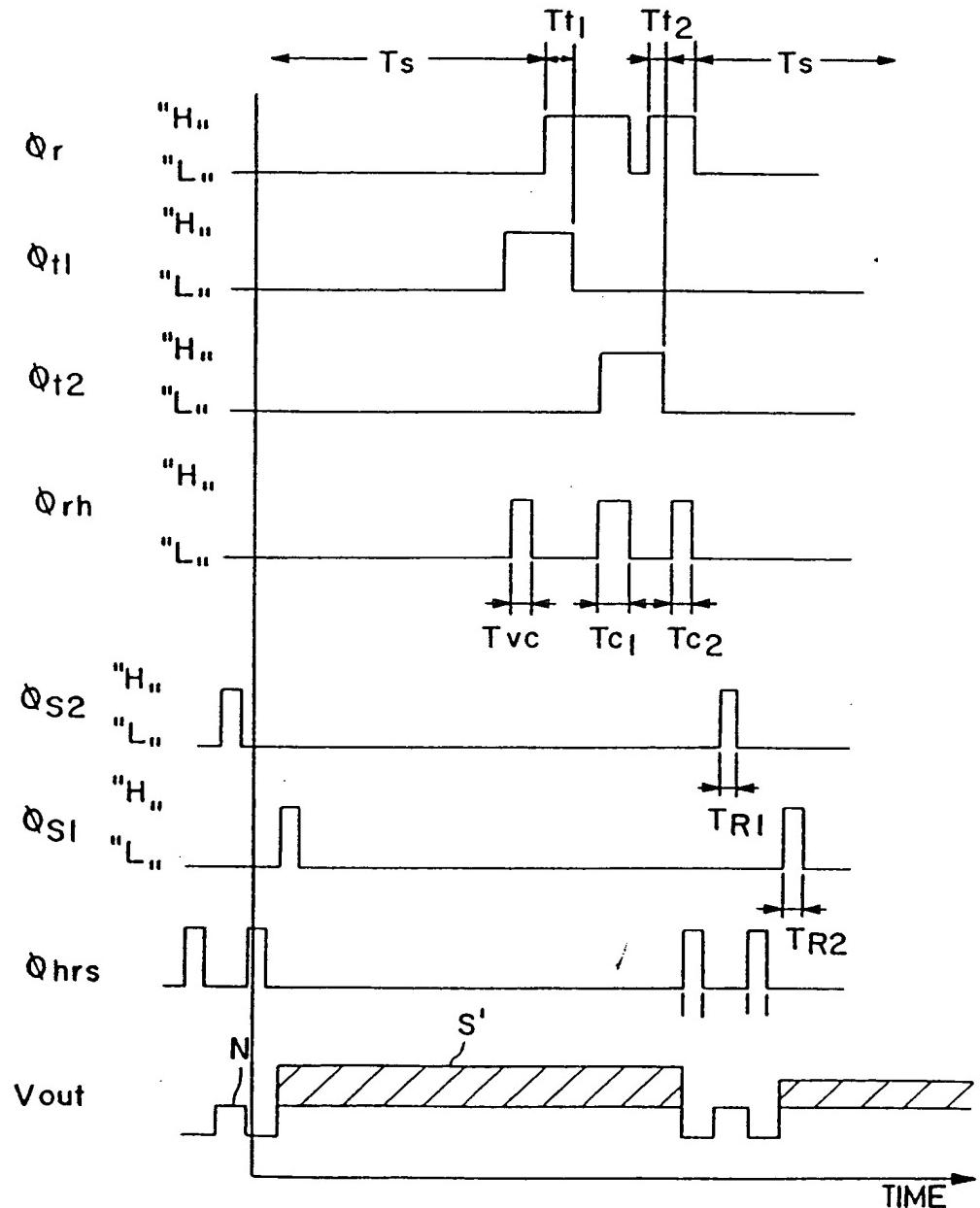
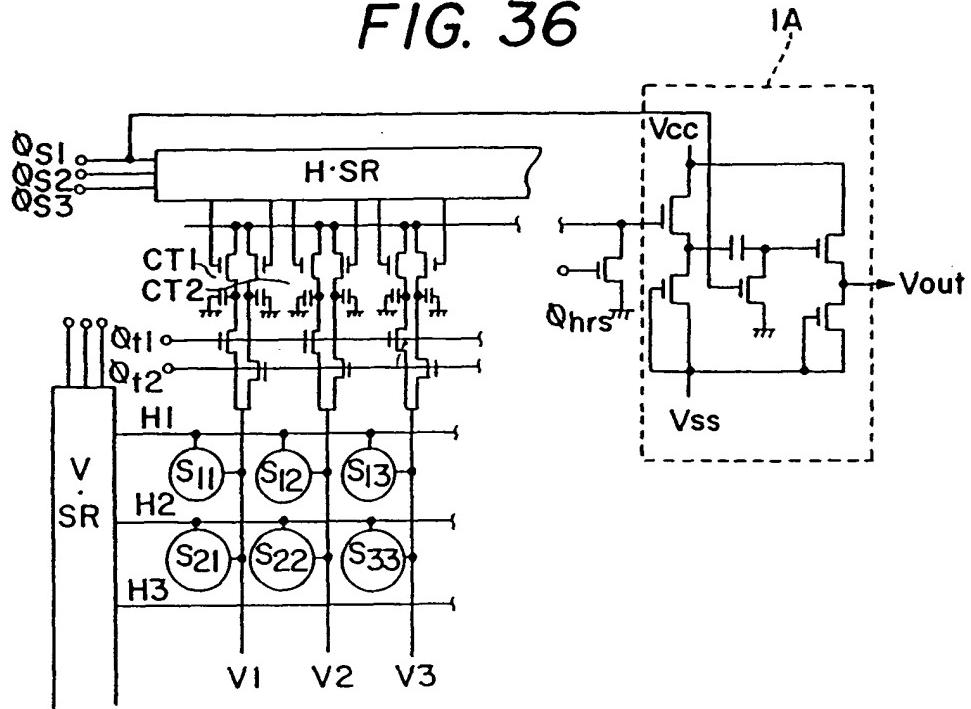


FIG. 35



*FIG. 36*



*FIG. 37*

